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 Zeng Jianping; Li Yu; Xie Haiping; Wen Jian;  
ASIC, 2005. ASICON 2005. 6th International Conference On  
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 Digital Object Identifier 10.1109/ICASIC.2005.1611312  
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- ☒ 4. **Low-voltage bandgap reference with temperature compensation based on a threshold voltage technique**  
 Bendali, A.; Savaria, Y.;  
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on  
 Volume 3, 26-29 May 2002 Page(s):III-201 - III-204 vol.3  
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